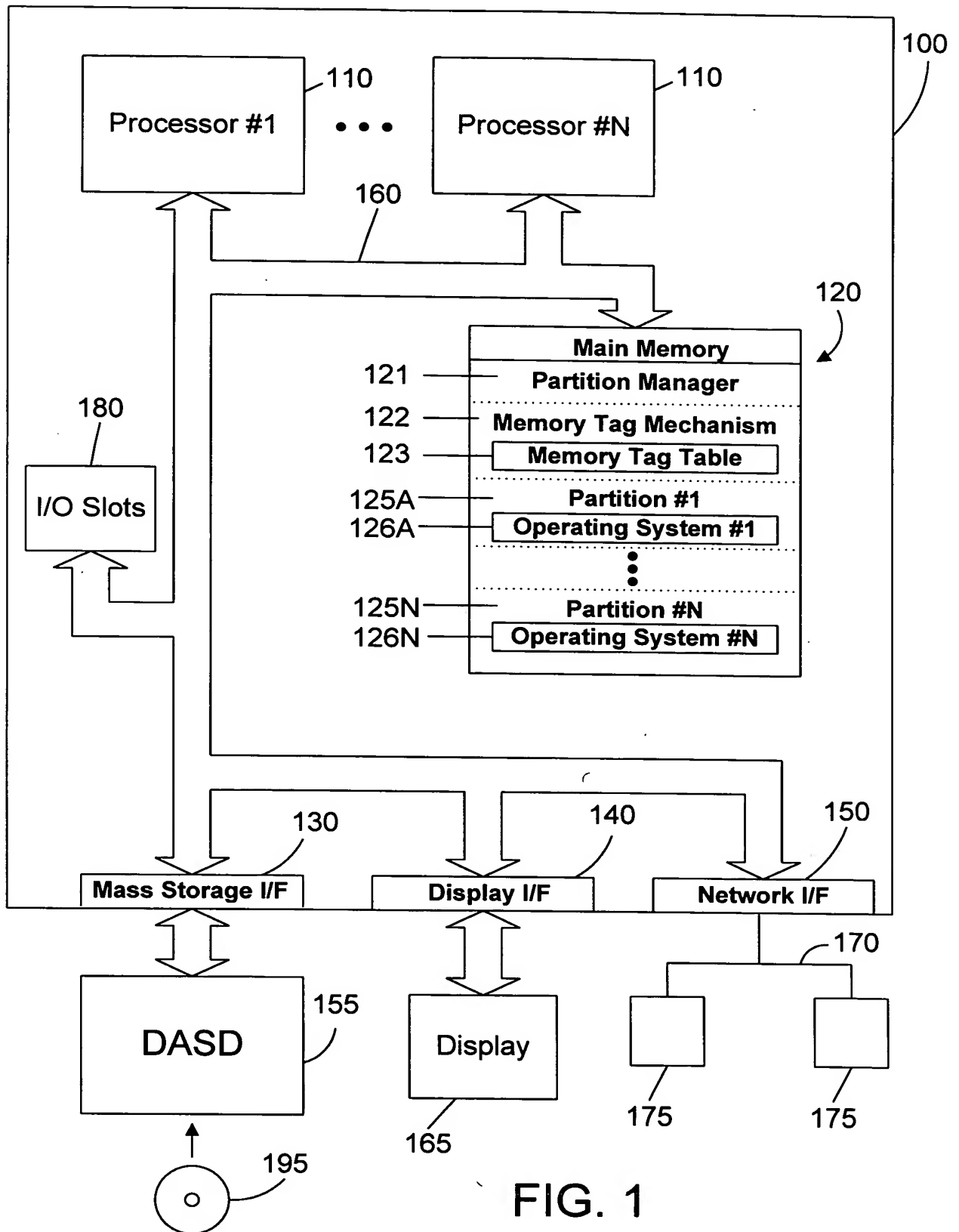


1/7



2/7

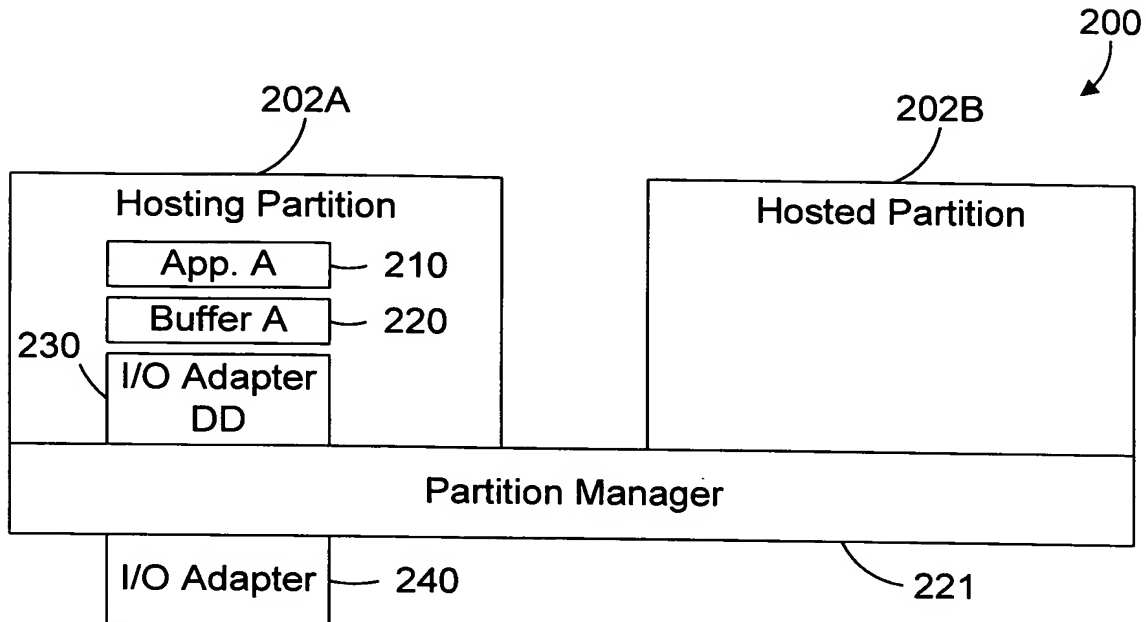


FIG. 2 Prior Art

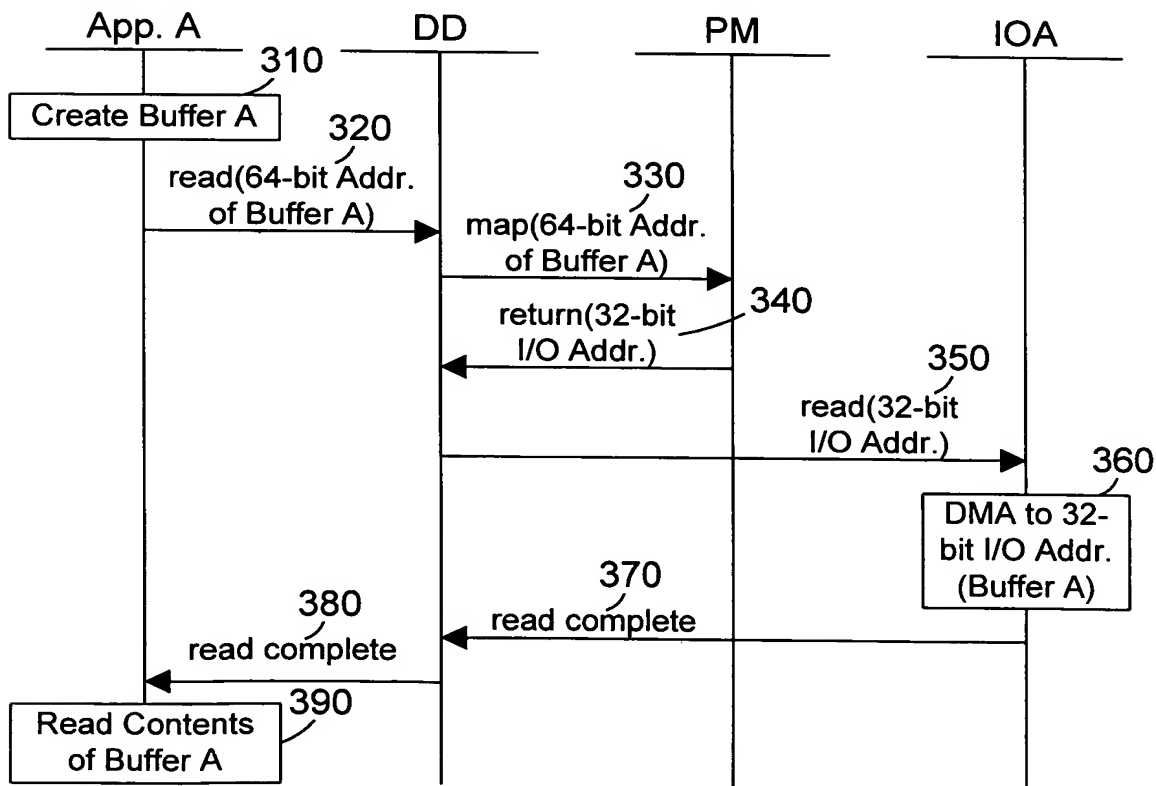


FIG. 3 Prior Art

3/7

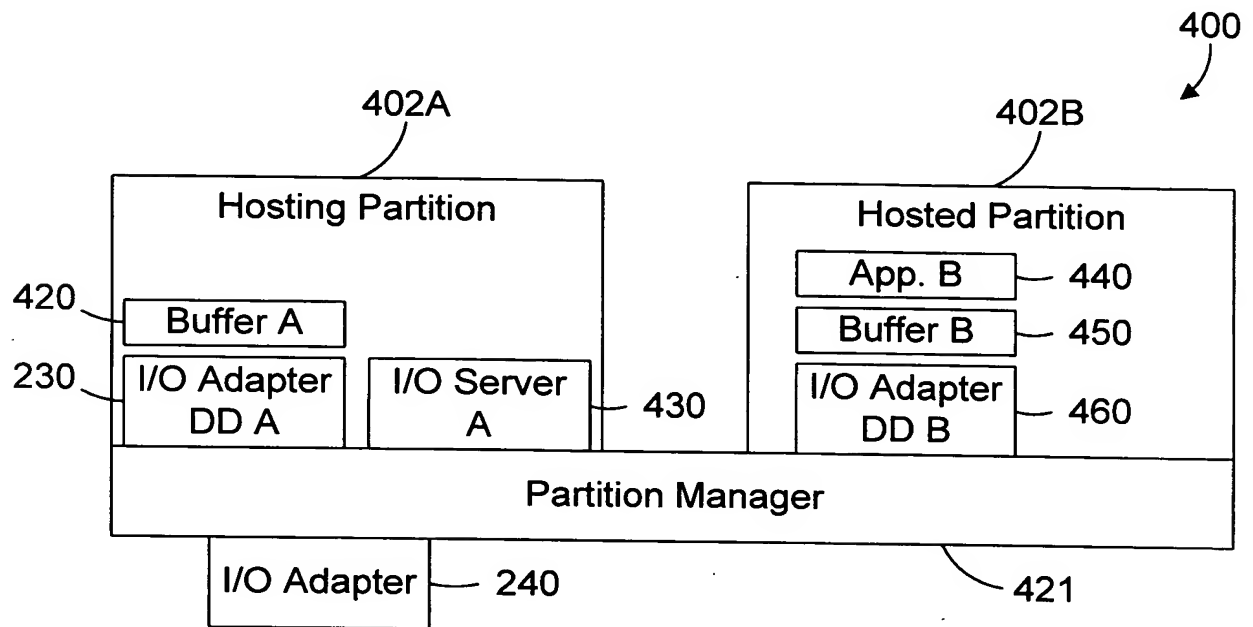


FIG. 4 Prior Art

4/7

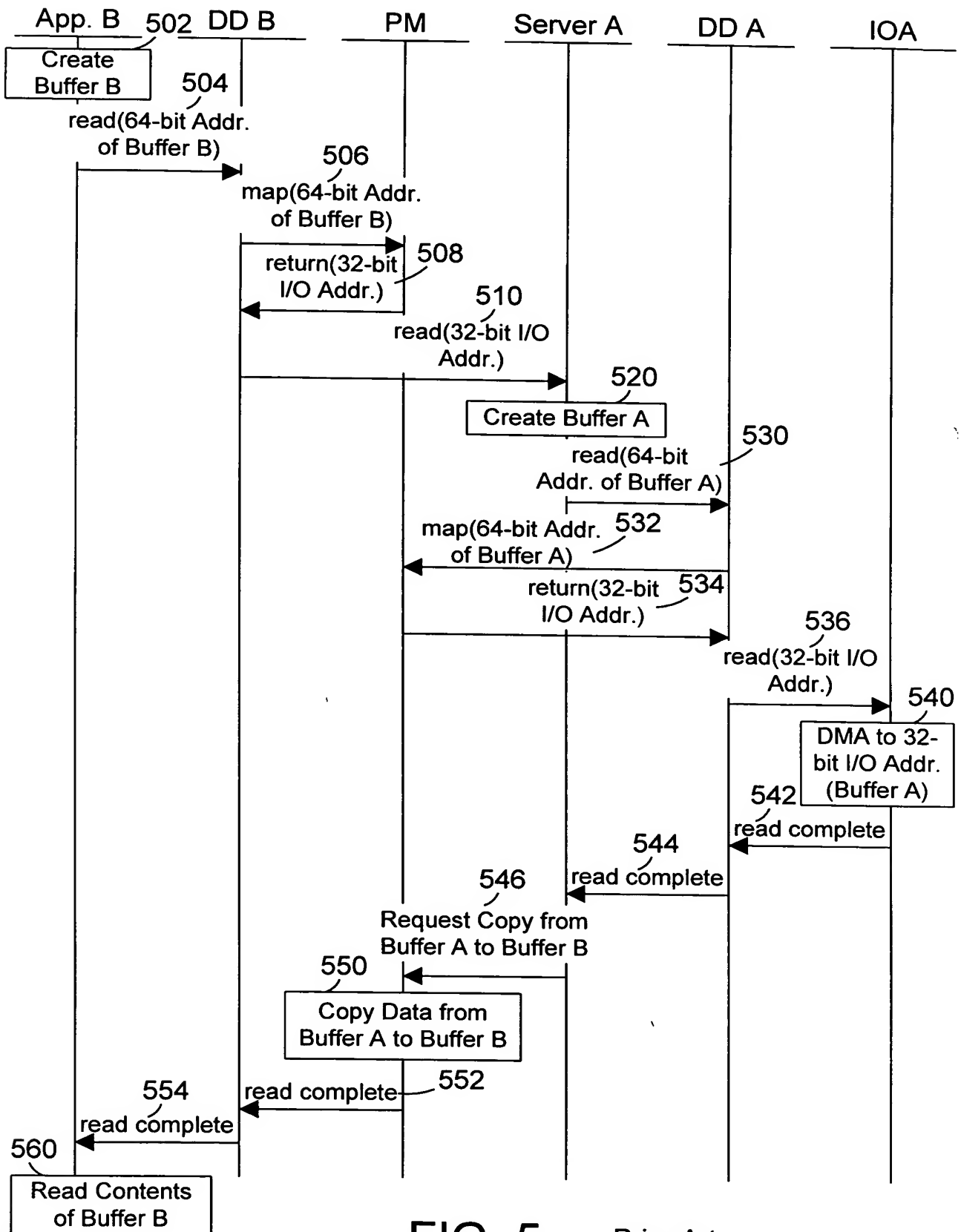


FIG. 5

Prior Art

5/7

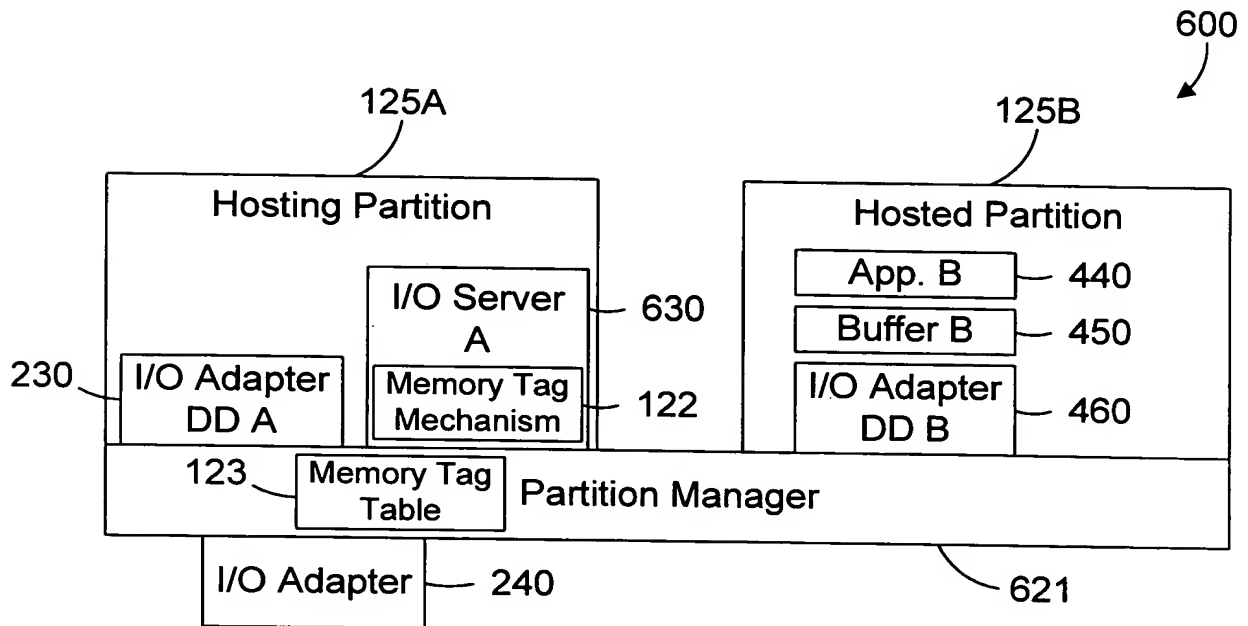


FIG. 6

6/7

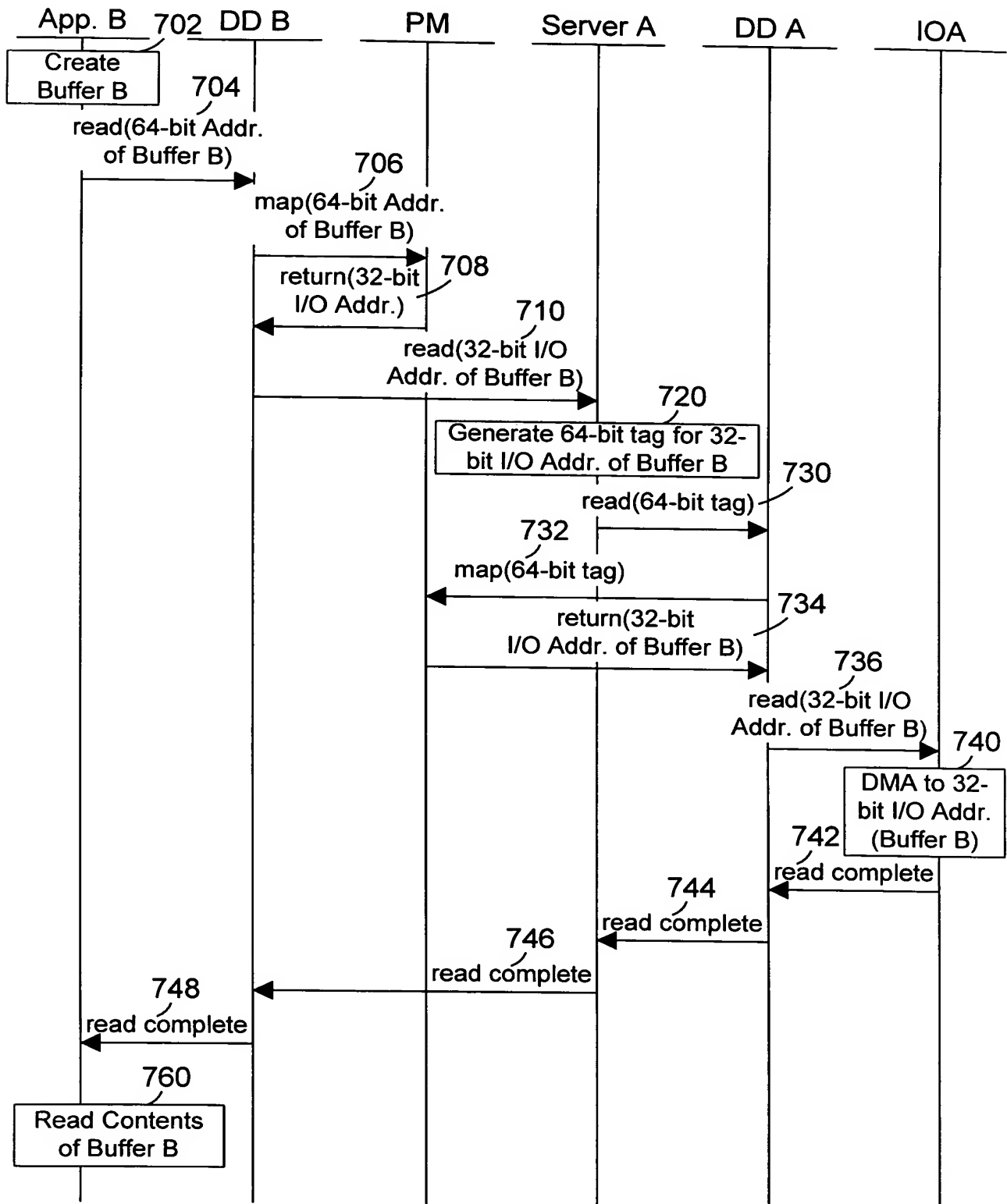
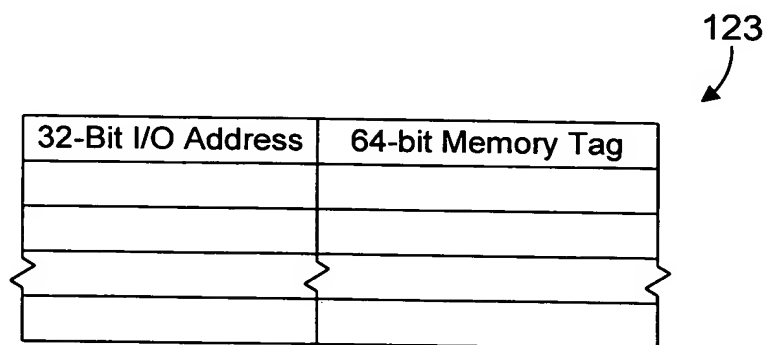


FIG. 7

7/7

123



32-Bit I/O Address	64-bit Memory Tag

FIG. 8